Amendments to the Claims:

The following Listing of Claims will replace all prior versions and listings of claims in the application:

Listing of Claims

1. (Currently amended) A sample processing device comprising:

a rectangular body ~~comprising four identifiable corners at the junctions of four identifiable sides and two major surfaces~~; and

a plurality of process arrays located within the body, each of the process arrays comprising an input cha mber, an output chamber, and a primary process chamber located between the input chamber and the output chamber, wherein the primary process chambers of the plurality of process arrays are arranged in a circular arc, and wherein the input chamber, the output chamber, and the primary process chamber of each process array are interconnected;

wherein the input chambers of the plurality of process arrays are arranged such that the spacing between each input chamber is regular in both the x and y direction in a rectilinear grid array, and further wherein the output chambers of the process arrays of the plurality of process arrays are arranged such that the spacing between each output chamber is regular in both the x and y direction in a rectilinear grid array.

2. (Original) A device according to claim 1, wherein the input chambers of at least two of the process arrays comprise a common input chamber.

3. (Previously Presented) A device according to claim 1, the device further comprising a secondary process chamber located between the primary process chamber and the output chamber of each of the process arrays, wherein the secondary process chambers of the process arrays are arranged in a circular arc, and wherein the secondary process chamber of each process array is interconnected with the primary process chamber and the output chamber located proximate the secondary process chamber of each process array.

4. (Original) A device according to claim 3, wherein the circular arc of the primary process chambers and the circular arc of the secondary process chambers are concentric arcs.

5. (Original) A device according to claim 1, wherein the input chambers of the plurality of process arrays are arranged along a straight line.

6. (Currently amended) A sample processing device comprising:

a rectangular body ~~comprising four identifiable corners at the junctions of four identifiable sides and two major surfaces~~; and

a plurality of process arrays located within the body, each of the process arrays comprising an input chamber, an output chamber, and a primary process chamber located between the input chamber and the output chamber, wherein the input chamber, the output chamber, and the primary process chamber of each process array are interconnected;

wherein the primary process chambers of the plurality of process arrays are arranged in a circular arc;

and wherein the output chambers of the process arrays of the plurality of process arrays are arranged such that the spacing between each input chamber is regular in both the x and y direction in a rectilinear grid array;

and further wherein the input chambers of the plurality of process arrays are arranged such that the spacing between each output chamber is regular in both the x and y direction in a rectilinear grid array, and wherein the input chambers of at least two of the process arrays comprise a common input chamber.

7. (Previously Presented) A device according to claim 6, the device further comprising a secondary process chamber located between the primary process chamber and the output chamber of each of the process arrays, wherein the secondary process chambers of the process arrays are arranged in a circular arc, and wherein the secondary process chamber of each process array is interconnected with the primary process chamber and the output chamber located proximate the secondary process chamber of each process array.

8. (Original) A device according to claim 7, wherein the circular arc of the primary process chambers and the circular arc of the secondary process chambers are concentric arcs.

9. (Original) A device according to claim 6, wherein the input chambers of the plurality of process arrays are arranged along a straight line.

10. (Currently amended) A sample processing device comprising:

a rectangular body ~~comprising four identifiable corners at the junctions of four identifiable sides and two major surfaces~~; and

a plurality of process arrays located within the body, each of the process arrays comprising an input chamber, an output chamber, a primary process chamber located between the input chamber and the output chamber, and a secondary process chamber located between the primary process chamber and the output chamber, wherein the primary process chambers of the plurality of process arrays are arranged in a circular arc, and wherein the input chamber, the output chamber, the primary process chamber, and the secondary process chamber of each process array are interconnected;

wherein the input chambers of the plurality of process arrays are arranged such that the spacing between each input chamber is regular in both the x and y direction in rectilinear grid array, and further wherein the output chambers of the process arrays of the plurality of process arrays are arranged such that the spacing between each output chamber is regular in both the x and y direction in a rectilinear grid array.

11. (Original) A device according to claim 10, wherein the input chambers of at least two of the process arrays comprise a common input chamber.

12. (Original) A device according to claim 10, wherein the input chambers of the plurality of process arrays are arranged along a straight line.

13. (Currently amended) A sample processing device comprising:

a rectangular body ~~comprising four identifiable corners at the junctions of four identifiable sides and two major surfaces~~; and

a plurality of process arrays located within the body, each of the process arrays comprising an input chamber, an output chamber, and a primary process chamber located between the input chamber and the output chamber, wherein the primary process chambers of the plurality of process arrays are arranged in a circular arc, and wherein the input chambers of at least two of the process arrays comprise a common input chamber, and wherein the input chamber, the output chamber, and the primary process chamber of each process array are interconnected;

wherein the input chambers of the plurality of process arrays are arranged such that the spacing between each input chamber is regular in both the x and y direction in a rectilinear grid array, and further wherein the output chambers of the process arrays of the plurality of process arrays are arranged such that the spacing between each output chamber is regular in both the x and y direction in a rectilinear grid array.

14. (Previously Presented) A device according to claim 13, the device further comprising a secondary process chamber located between the primary process chamber and the output chamber of each of the process arrays, wherein the secondary process chambers of the process arrays are arranged in a circular arc, and wherein the secondary process chamber of each process array is interconnected with the primary process chamber and the output chamber located proximate the secondary process chamber of each process array.

15. (Original) A device according to claim 14, wherein the circular arc of the primary process chambers and the circular arc of the secondary process chambers are concentric arcs.

16. (Original) A device according to claim 13, wherein the input chambers of the plurality of process arrays are arranged along a straight line.

17. (Currently amended) A sample processing device comprising:

a rectangular body ~~comprising four identifiable corners at the junctions of four identifiable sides and two major surfaces~~; and

a plurality of process arrays located within the body, each of the process arrays comprising an input chamber, an output chamber, and a primary process chamber located between the input chamber and the output chamber, wherein the primary process chambers of the plurality of process arrays are arranged in a circular arc, and wherein the input chambers of all of the process arrays comprise a common input chamber, and wherein the input chamber, the output chamber, and the primary process chamber of each process array are interconnected;

wherein the output chambers of the process arrays of the plurality of process arrays are arranged such that the spacing between each output chamber is regular in both the x and y direction in a rectilinear grid array.

18. (Previously Presented) A device according to claim 17, the device further comprising a secondary process chamber located between the primary process chamber and the output chamber of each of the process arrays, wherein the secondary process chambers of the process arrays are arranged in a circular arc, and wherein the secondary process chamber of each process array is interconnected with the primary process chamber and the output chamber located proximate the secondary process chamber of each process array.

19. (Original) A device according to claim 18, wherein the circular arc of the primary process chambers and the circular arc of the secondary process chambers are concentric arcs.

20. (Previously Presented) The device according to claim 1, wherein at least one of the plurality of process arrays further comprises at least one valve located between the input chamber and the output chamber.

21. (Previously Presented) The device according to claim 6, wherein at least one of the plurality of process arrays further comprises at least one valve located between the input chamber and the output chamber.

22. (Previously Presented) The device according to claim 10, wherein at least one of the plurality of process arrays further comprises at least one valve located between the input chamber and the output chamber.

23. (Previously Presented) The device according to claim 13, wherein at least one of the plurality of process arrays further comprises at least one valve located between the input chamber and the output chamber.

24. (Previously Presented) The device according to claim 17, wherein at least one of the plurality of process arrays further comprises at least one valve located between the input chamber and the output chamber.